

Memo/Reply From  
JOSHUA LEDERBERG

TO: Bob Joyce

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The literature I got was very exciting, though some of it, predictably, is over my head. The level of explication in what I saw made it hard for me to tell whether the local processors had enough cache memory (or enough general registers) to minimize contention on the bus when you have a significant number (think of  $10^2$ ....) processors on it.

But thanks you.

I certainly enjoyed our conversation; and whether we can conveniently revisit in SFO on 8/17, I hope we intersect

again soon.

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