

Memo/Reply From
JOSHUA LEDERBERG

TO: Bob Noyce

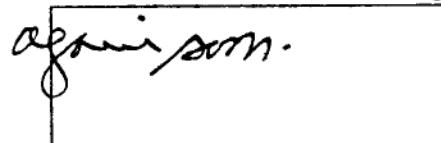
JUL 31 1981

APX-432

The literature I got was very exciting, though some of it, predictably, is over my head. The level of explication in what I saw made it hard for me to tell whether the local processors had enough cache memory (or enough general registers) to minimize contention on the bus when you hang a significant number (think of $10^2 \dots$) processors on it.

But thanks you.

I certainly enjoyed our conversation; and whether we can conveniently reign in SF on 8/17, I hope we intersect again soon.



Joshua Lederberg
President
The Rockefeller University
New York, N.Y. 10021
(212) 360-1234

ORIG.
 COPY RETAINED

- REPLY FORM -